



PATENT
Docket No. SPLX.P0050

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Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on February 11, 2004


John Stattler

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Jun Li, et al.

Serial No.: 09/854,146

Filing Date: 05/11/2001

For: HIGH ACCURACY TIMING MODEL
FOR INTEGRATED CIRCUIT
VERIFICATION

ISSUE FEE TRANSMITTAL

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

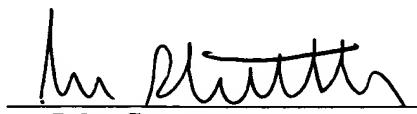
1. Fee Transmittal Form;
2. Statement Under 37 CFR 3.73(B), accompanied by a copy of an Assignment and a Certificate of Merger merging Simplex Solutions, Inc. into Cadence Design Systems, Inc. (Copy of the Certificate of Merger is being forwarded concurrently herewith to the Assignment branch for recordation.)
3. A Credit Card Payment Form.

In the unlikely event that the attached credit card payment form is separated from the above-mentioned documents, or the Office deems that the funds authorized therein are

insufficient for any reason, applicant hereby petitions for any needed relief and authorizes the Office to charge any additional fees that may be required by this transmittal, or to credit any overpayment to **Deposit Account No. 50-1128** referencing SPLX.P0050.

Dated: February 11, 2004

Respectfully submitted,

By: 
John Stattler
Registration No. 36,285

Stattler, Johansen & Adeli LLP
P.O. Box 51860
Palo Alto, CA 94303-0728

Phone No. (650) 752-0990 x100
Fax No. (650) 752-0995